



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Randolph
 Serial No.: 10/032,646
 Conf. No.: 5662
 Filed: December 27, 2001
 Title: PLANAR TRANSISTOR STRUCTURE USING ISOLATION IMPLANTS FOR
 IMPROVED VSS RESISTANCE AND FOR PROCESS SIMPLIFICATION
 Art Group: 2811
 Examiner: Loke, Steven Ho Yin

H/C/A
 Bond
 J. McMillan
 7/29/03

Commissioner of Patents

P.O. Box 1450
 Alexandria, VA 22313-1450

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I hereby certify this document is being deposited with sufficient postage, with the U.S. Postal Service as U.S. Express mail post office to Addressee service, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on or before the date indicated below:

<i>Toni Stanley</i> Signature	TONI STANLEY Printed Name	June 30, 2003 Date
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RESPONSE TO OFFICE ACTION

07/30/2003 JMCMILLA 00000008 010365 10032646

01 FC:1201 160.04 D4 In response to the Office Action mailed January 28, 2003 (Paper 4), with a three-month shortened statutory period for response set to expire on April 28, 2003, and hereby extended to June 30, 2003 by the attached Petition for Extension of Time, Applicant respectfully requests that